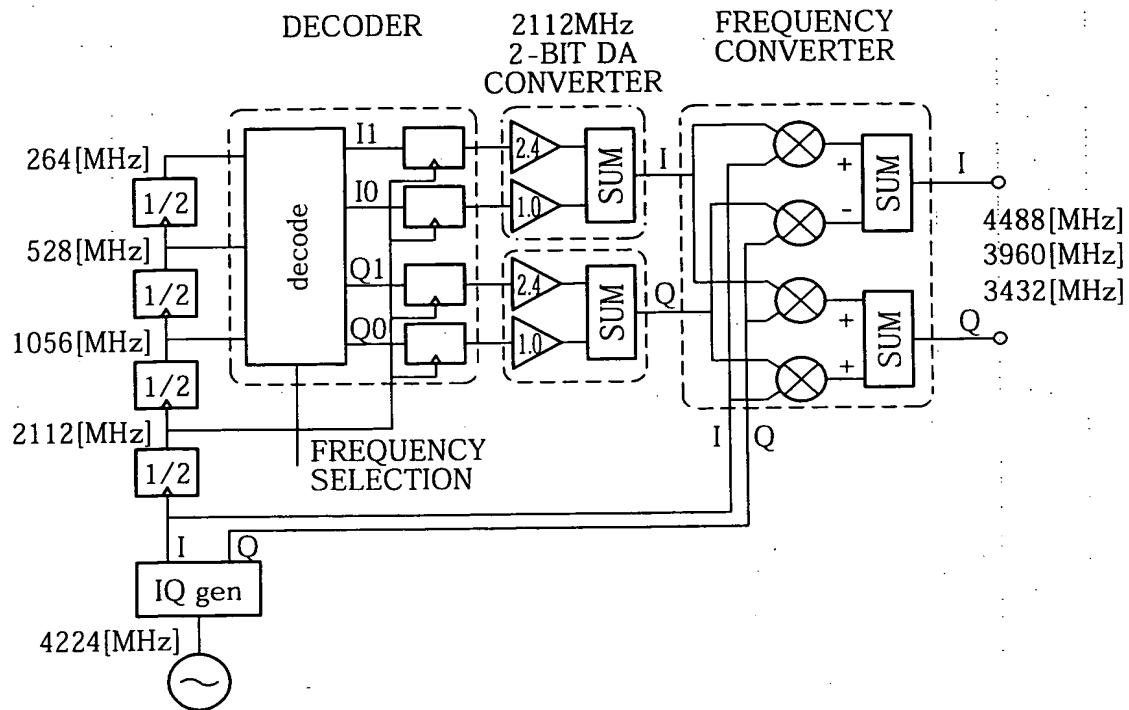


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F | G. 1



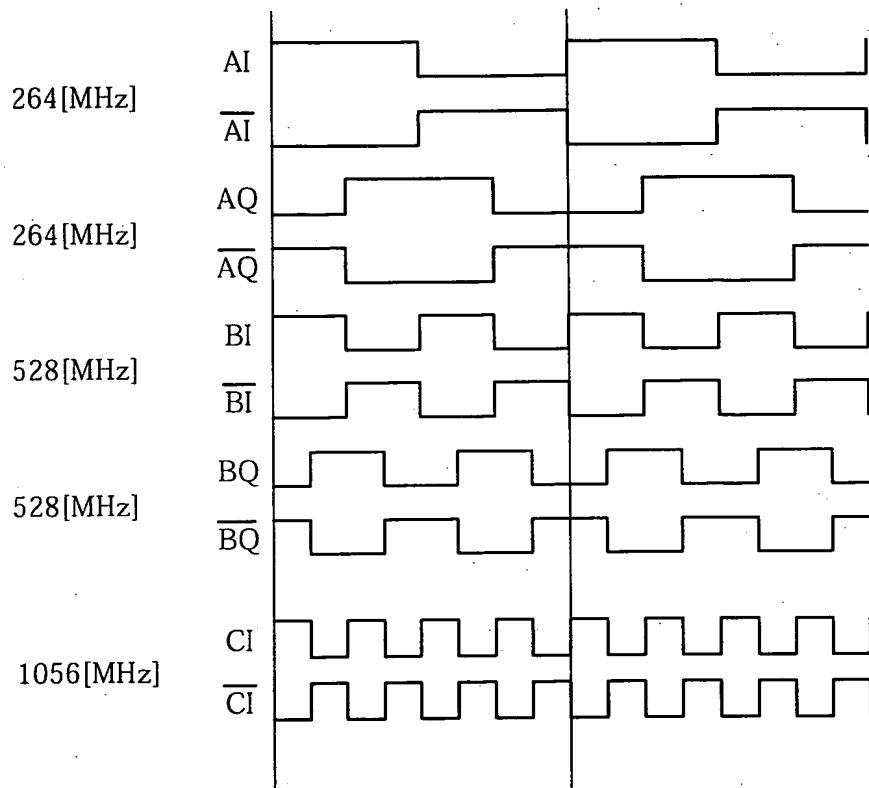
F | G. 2

TRUTH TABLE OF DECODER

8divider	+264[MHz]	-264[MHz]	+792[MHz]	-792[MHz]
000	(11,10)	(11,01)	(10,11)	(10,00)
001	(10,11)	(10,00)	(00,01)	(00,10)
010	(01,11)	(01,00)	(11,01)	(11,10)
011	(00,10)	(00,01)	(01,11)	(01,00)
100	(00,01)	(00,10)	(01,00)	(01,11)
101	(01,00)	(01,11)	(11,10)	(11,01)
110	(10,00)	(10,11)	(00,10)	(00,01)
111	(11,01)	(11,10)	(10,00)	(10,11)

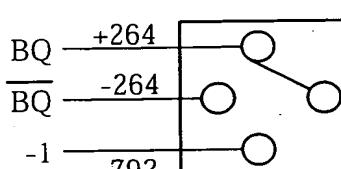
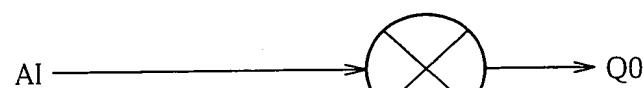
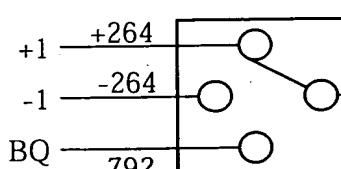
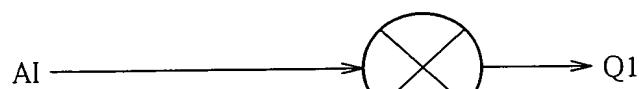
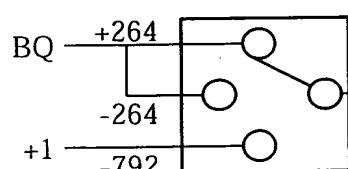
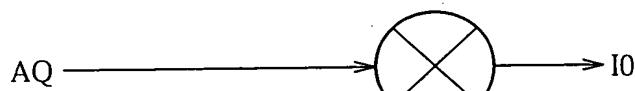
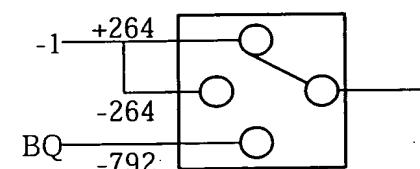
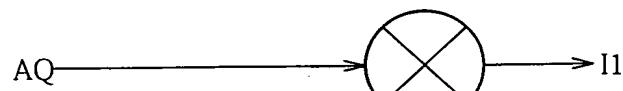
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## F I G. 3



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## F | G. 4



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## F I G. 5

COMPLEX AMPLITUDE VALUES OF DA CONVERTERS

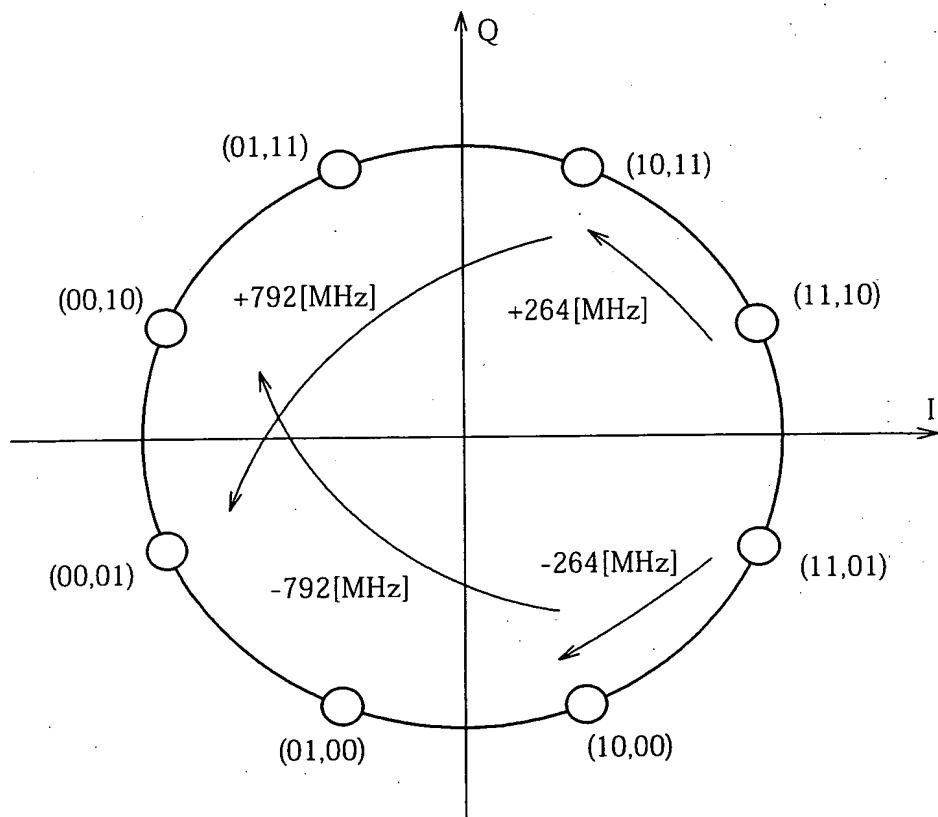
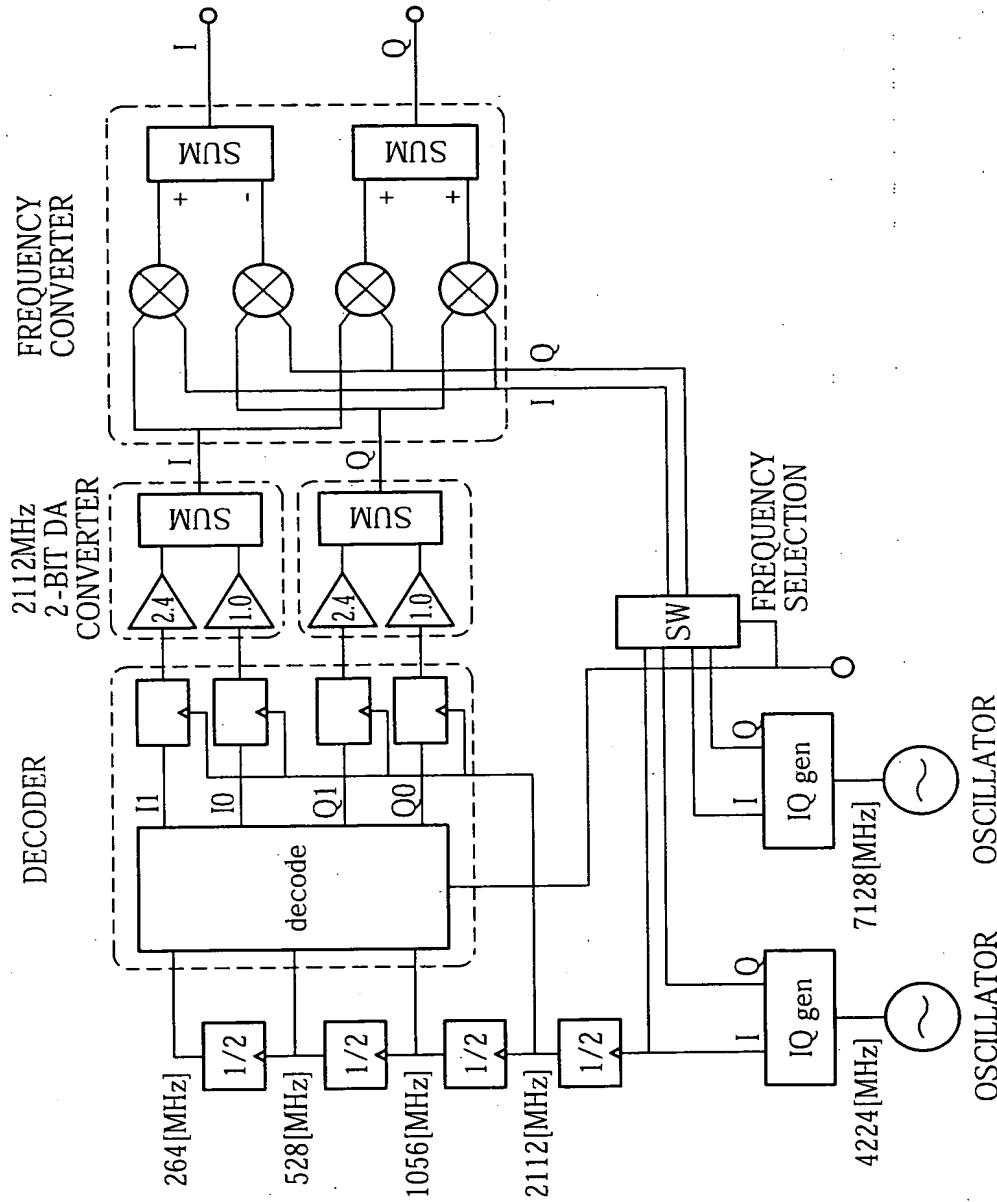
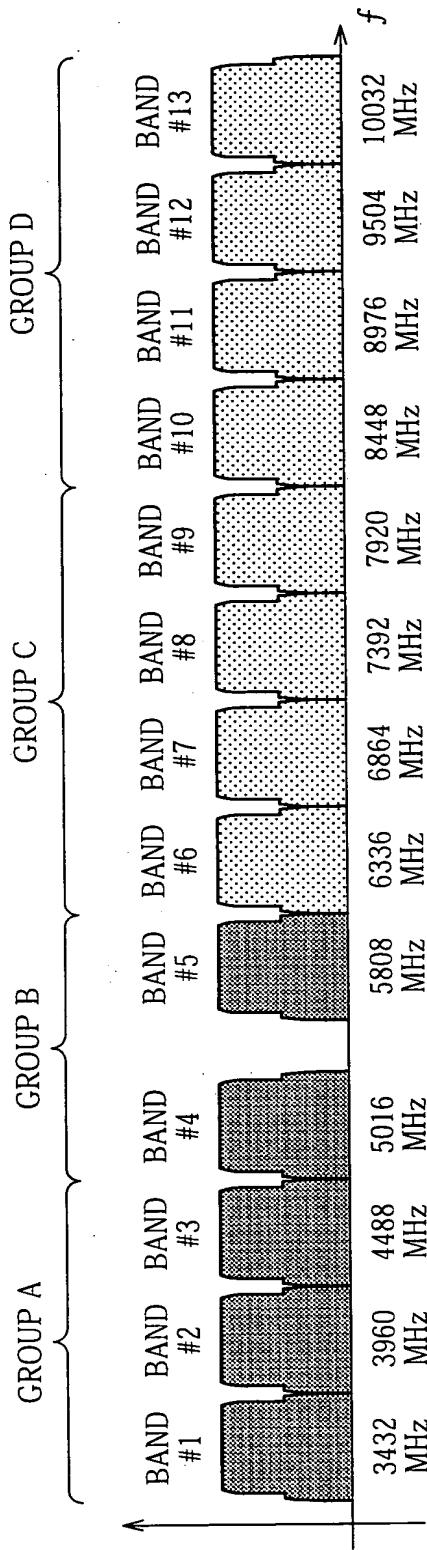


FIG. 6



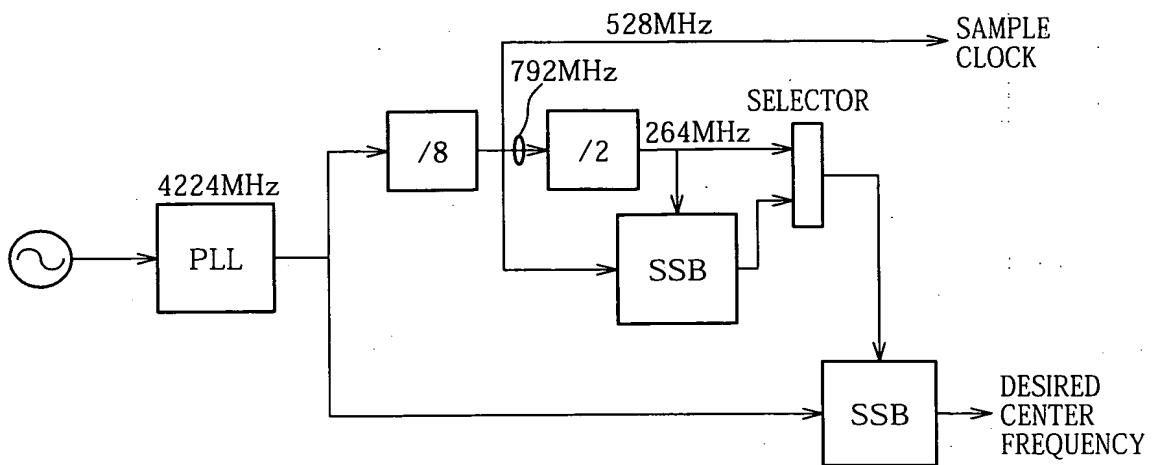
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F I G. 7

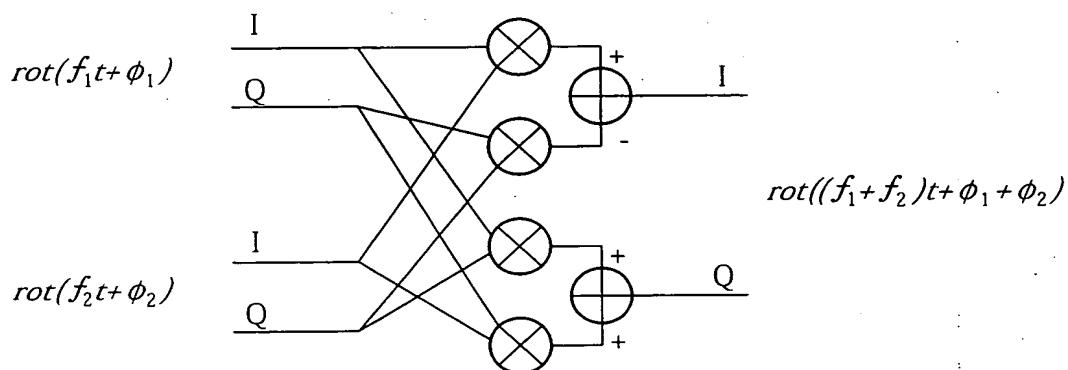


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F I G. 8



F I G. 9



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E - G 10

